



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Patrizio Vinciarelli et al.      Art Unit : 2836  
Serial No. : 09/841,471      Examiner : Boris Benenson  
Filed : April 24, 2001  
Title : COMPONENTS HAVING ACTIVELY CONTROLLED CIRCUIT ELEMENTS

**MAIL STOP 313(c)**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicant submits the references listed on the attached form PTO-1449.

This filing is being made with the filing of a Request for Continued Examination. No fee is required.

Respectfully submitted,

Date: \_\_\_\_\_

7/14/04

Stephen L. Romine  
Reg. No. 43,056

Fish & Richardson P.C.  
225 Franklin Street  
Boston, MA 02110-2804  
Telephone: (617) 542-5070  
Facsimile: (617) 542-8906

20898354.doc

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date of Deposit

July 14, 2004

Signature

Toni M. Sousa

Typed or Printed Name of Person Signing Certificate

|   |   |                                       |                               |
|---|---|---------------------------------------|-------------------------------|
| Substitute Form PTO-1449<br>(Modified)<br>JUL 16 2004<br>(37 CFR § 1.98(b)) | U.S. Department of Commerce<br>Patent and Trademark Office                                    | Attorney's Docket No.<br>00614-120001 | Application No.<br>09/841,471 |
|   | <b>Information Disclosure Statement<br/>by Applicant</b><br>(Use several sheets if necessary) |                                       |                               |
|   | Applicant<br>Patrizio Vinciarelli et al.  |                                       | Filing Date<br>April 24, 2001 |

### U.S. Patent Documents

| Examiner Initial | Desig. ID | Document Number | Publication Date | Patentee         | Class | Subclass | Filing Date If Appropriate |
|------------------|-----------|-----------------|------------------|------------------|-------|----------|----------------------------|
|                  | AA        | 5,734,259       | 03/31/1998       | Sisson et al.    |       |          |                            |
|                  | AB        | 5,831,842       | 11/03/1998       | Ogasawara et al. |       |          |                            |
|                  | AC        | 5,939,867       | 08/17/1999       | Capici et al.    |       |          |                            |
|                  | AD        | 6,232,755       | 05/15/2001       | Zhang            |       |          |                            |
|                  | AE        | 6,236,194       | 05/22/2001       | Manabe et al.    |       |          |                            |
|                  | AF        | 2001/0045863    | 11/29/2001       | Pelly            |       |          |                            |

### Other Documents (include Author, Title, Date, and Place of Publication)

| Examiner Initial | Desig. ID | Document  |
|------------------|-----------|---|
|                  | AG        | Julian et al., "Active Filtering for Common Mode Conducted EMI Reduction in Voltage Source Inverters", ( <i>APEC Thirteenth Annual Applied Power Electronics Conference and Exposition</i> ), Sponsored by IEEE Power Electronics Society; IEEE Industry Applications Society Power Sources Manufacturers Association, Vol. 2, pp. 934-939 (1998) |
|                  | AH        | LaWhite et al., "Active Filters for 1-MHz Power Circuits with Strict Input/Output Ripple Requirements", <i>IEEE Transactions On Power Electronics</i> , Vol. PE-2:4, pp. 282-290 (1987)   |
|                  | AI        | LaWhite, Leif E., "Active Filters for 1MHz Power Circuits Under Strict Ripple Limitations", © Massachusetts Institute of Technology, pp. 1-99 (February, 1987)  |
|                  | AJ        | LaWhite et al., "Design of Active Ripple Filters for Power Circuits Operating in the 1-10 MHz Range", <i>IEEE Transactions On Power Electronics</i> , Vol. 3:3, pp. 310-317 (1988)  |
|                  | AK        | Ogasawara et al., "An Active Circuit for Cancellation of Common-Mode Voltage Generated by a PWM Inverter", <i>IEEE Transactions On Power Electronics</i> , Vol. 13:5, pp. 835-841 (1998)  |
|                  | AL        | Ott, Henry W., "Noise Reduction Techniques In Electronic Systems", 2 <sup>nd</sup> Edition, Published by John Wiley & Sons, Inc., TABLE OF CONTENTS (8 pages) (1936)  |
|                  | AM        | Poon et al., "Techniques for Input Ripple Current Cancellation: Classification and Implementation", <i>IEEE Transactions On Power Electronics</i> , Vol. 15:6, pp. 1144-1152 (2000)   |
|                  | AN        | Son et al., "A Novel Active Common-mode EMI Filter for PWM Inverter", <i>IEEE</i> , pp. 545-549 (2002)  |

|  |                 |
|--|-----------------|
| Examiner Signature   | Date Considered |
| EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. |                 |